1/14 ... New sct drawings

FIG. 1A (PRIOR ART)

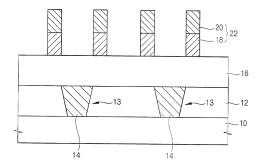
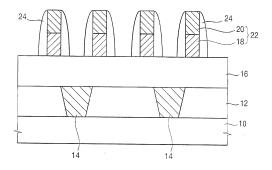


FIG. 1B (PRIOR ART)



New set drawings

FIG. 1C (PRIOR ART)

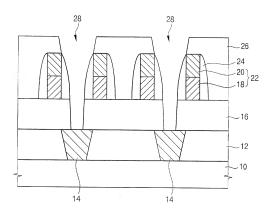


FIG. 2A

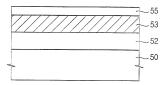
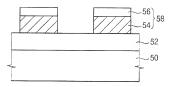


FIG. 2B



Ju-Yong LEE, et al.
SEMICONDUCTOR DEVICE AND METHOD OF ANUFACTURING THE SAME
Attorney Docket No. 2522-036/Application No. 10/719,624

FIG. 2C

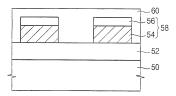


FIG. 2D

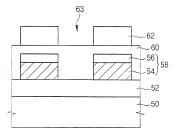


FIG. 2E

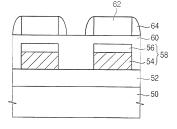


FIG. 2F

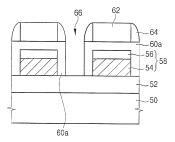


FIG. 3A

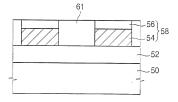


FIG. 3B

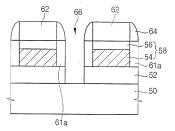


FIG. 4

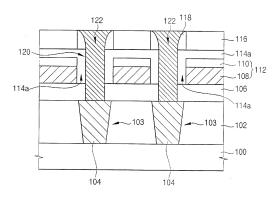


FIG. 5A

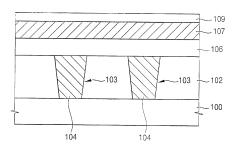


FIG. 5B

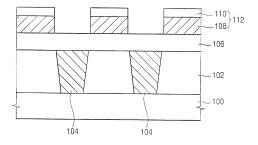


FIG. 5C

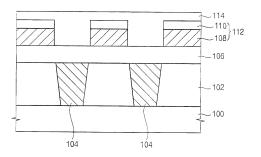
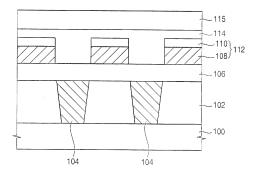


FIG. 5D



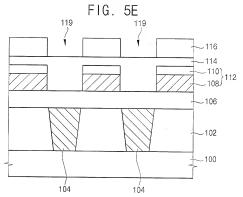


FIG. 5F

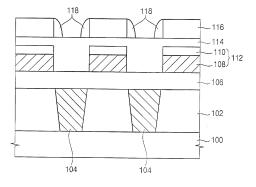


FIG. 5G

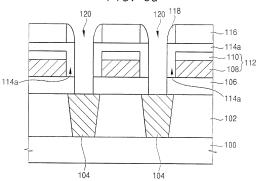
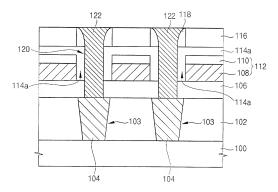


FIG. 5H





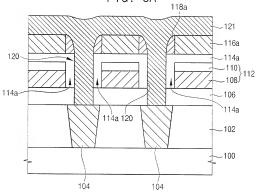
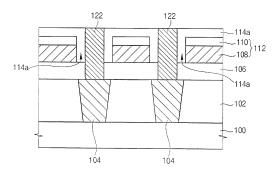


FIG. 6B



New set drawings

FIG. 7A

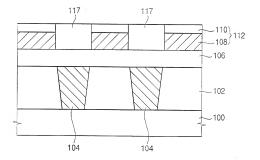


FIG. 7B

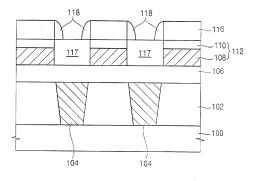


FIG. 7C

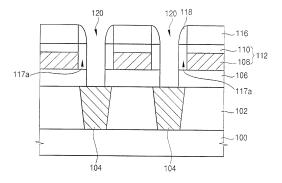


FIG. 7D

